## IN THE SPECIFICATION:

At page 2, the paragraph beginning at line 10 has been rewritten as follows:

--Yet another characteristic feature of the present invention is that in the [[even]] event that no data is available in the transmitter to be sent upon receipt of the trigger signals, the transmitter is able to generate idle data and to send this idle data to the receiver. In this way, e.g., the frame synchronization process is not disturbed.--.

At page 4, the paragraph beginning at line 9 has been rewritten as follows:

--The ADSL modem includes besides a receiver RX also the characteristic functional blocks of an ADSL modem. Since the description of the ADSL technology goes beyond the scope of this invention, these functional blocks are not shown in the figure. However, it is worth mentioning here that one of the functional elocks blocks of such an ADSL modem is a framer which organizes overhead information and user information, i.e., the incoming data DAT into ADSL frames, i.e., uniformly sized groups of bits used to organize the ADSL data stream.--.

At page 4, the paragraph beginning at line 27 has been rewritten as follows:

--It has to be remarked here that the trigger signals T are allowed to be of any kind, e.g., one single bit pulse or a predefined codeword as long as the trigger generating means T-GEN of the receiver RX and the trigger receiving means T-RX of the transmitter TX can recognize the trigger signals T.--.